

	P-78: Specially Designed Grounding , Earthing and Isolation Schemes at EMP Building for it's Effective Sheilding and for operation of Test Set-ups K. R. Rao, G. V. Reddy, M. Halder and R. Rajyalakshmi RCI, Hyderabad, India	P-36: Implanted Antennas Inside The Human Body: Design, Simulations With In Mics Band Limit M. A. Kumar, S. K. Mandal and G. S. N. Raju CUTM, India; AUCE, India	
17:30-18:30	Session 10: Poster Session Chairman: Mr. B N Prakash, DGM, Robert Bosch Engineering & Engineering, Solutions Pvt Ltd, Bengaluru		
	P-33: Low Conducted EMI in NUPF Converters ; M. Ashritha and M. L. Sudheer, UVCE, Bengaluru, India		
	P-20: Patterns of Thinned Arrays; B. S. Rao and G. S. N. Raju ,GVPTC, India; AU, India		
	P-70: Design of Ground Plane of a Slotted Microstrip Patch Antenna for EMI Rejection; A.Ghosal, A. Majumdar, S. K. Das and A. Das, GNIT, India		
	P-55: Radiated Immunity Assessment of Music Infotainment System and Recommending the Enclosure Design to Mitigate Effects; S. Jog and V. Bhatnagar, DYPIET, Bengaluru, India		
	P-54: Software frame work for ESD soft failures; S. D. Solanki and B. P. Subramanya, Intel, Bengaluru,India		
	P-59: Grounding Technique for EFT and Surge Test; S. Gulhane and A. Ratnaparakhe, Honeywell, Bengaluru, India		
	P-52: CPW Fed Square Patch Antenna for Biomedical Applications; S.Suganthi, M.Noorjahan and P.T. Selvan, KRCT, India		
	P-09: Design and Analysis of Dielectric Lens Folded Feed Half Impulse Radiating Antenna (FF-HIRA); S. J. Petkar, S. B. Umbarkar, F. S. Kazi and N. M. Singh, RAIT, India; VJTI, India		
	P-13: Comparative Analysis of Microstrip Moody Patch Antenna for Space Applications; D. Kaushal and T. Shanmuganatham, PU, India		
	P-23: A High Efficient Compact CPW fed Hexagonal Slot Antenna for Diversified Applications; V. Madhuri, S. A. Kumar and T. Shanmuganatham, Vel Tech, India; PU, India		
	P-53: Experimental analysis on the Result of shielded Transfer Impedance for RF cables; T.Pounraj and A. K. Swamy, TATA Power, Bengaluru,India		
	P-62: Methods to implement & qualify the circuit changes on Proto-PCB to reduce the ESD test cycle time; A.Kuruba, Honeywell, Bengaluru, India		
	P-31: Far Field Measurement Using Near Field Magnetic Probing Method; B. P.Subramanya, D. Sharma, Intel, Bengaluru, India		
19:00-20:15	Cultural Program		
20:15-22:00	Banquette		

Dec 9th 2016 (Friday)

09:00-10:00	Session 11: PCB level EMI/EMC Venue: Auditorium – I Chairman: Mr. P Radhakrishana, LRDE, Bengaluru	Session 12: ESD Venue: Auditorium – II Chairman: Capt (IN) P. P. Singh, NEC Mumbai	Session 13: Space Applications Venue: Auditorium – III Chairman: Mr. Colin Brench, Amphenol TCS in Nashua, New Hampshire	
	P-60: A case study on radiated emission in a high speed microprocessor based design S. Meloth Honeywell, Bengaluru , India	P-49: Noise Coupled to Unshielded Wire due to Computed Transient Electromagnetic Fields Generated by ESD Events in Spacecraft N. L. Nandini, G. S. Rao, V. Raghavaiah, P. Sowjanya, A. K.Varla, R. L. Mini,P. V. N. Murthy, R. Renuka, V. K. Hariharan & M.N. Rao, ISRO, Bengaluru ,India	P-46: NASA ISRO Synthetic Aperture RADAR (NISAR) Spacecraft Magnetic Field Studies V. A. Kumar, V. Raghavaiah, P. Sowjanya, P. V. N. Murthy andR. Renuka, V.K.Hariharan and M.N. Rao; ISRO, Bengaluru, India	
	P-05: PCB design guidelines of Smartphones and Tablets for EMC D. Sharma and G. Vanga Intel, Bengaluru, India	P-06: ESD Design Methodology for Tablets G. Vanga and Y. V. Waghela Intel, India	P-22: Design of CPW Fed Rectangular Shaped Slot Antenna for Satellite Applications M. A. Putta, S. A. Kumar and T. Shanmuganantham Vel Tech, India; PU, India	
	P-11: Switched-Mode Power Supply design guidelines for smartphones and tablets for reducing RF emissions S. D. Solanki and Y. V. Waghela, Intel, Bengaluru , India	P-66: Overview of ESD impacts over Industry Yield C. M. Somashekaraiah Honeywell, Bengaluru, India	P-44: Charging Analysis of GSAT-17 Patch Antenna P. V. N. Murthy, V. Raghavaiah, P. Sowjanya, V. A. Kumar, R. Renuka, V.K.Hariharan and M.N. Rao, ISRO, Bengaluru, India	
10:00-10:30	PLENARY TALK 2 Venue: Auditorium – I Topic: “High Power Electromagnetic Activities in Department of Atomic Energy” Speaker: Dr. Archana Sharma, Outstanding Scientist, BARC, DAE, India			
10:30-11:00	TEA BREAK			
11:00-12:20	Session 14: Lightning & High Power Electromagnetics Venue: Auditorium – I Chairman: Dr. Young-Ki Chung , South Korea	Session 15: EMC in Communication Systems Venue: Auditorium – II Chairman: Mr. PNAP Rao, Ex PD (Systems), ADA, Bengaluru	Venue: Auditorium – III	
	INVITED TALK 2 Title: High Power ElectroMagnetic (HPEM) Threat and Immunity Test Methods Speaker: Mr. Nicolas Mora, Montena, Switzerland	INVITED TALK 3 Title: 5 G Communication and Its Implication on EMC Speaker: Prof. C Murali, RIAS, Bengaluru		
	P-08: New Indirect Lightning Test Requirements For Aircraft and Surface Ships A. Matoi, EMC Partner, Switzerland	P-04: Providing EMI Protection and Addressing EMC Compatibility Issues in a Cognitive Radio Network R. Shishir, ADA, India		
	P-15: Characteristic Test of the Corona Discharge Initiating Voltage for the Bipolar Conventional Air Terminals K. Lee, C. Lee, B. Jeon, Y-K. Chung, B. Lee and J. Kim OMNI LPS, Korea; Inha Univ., Korea; Soongsil Univ., South Korea	P-29: Prediction and Analysis of Electromagnetic Interference in Radio Frequency Propagation for Transmitter - Receiver pair using LabVIEW M. S. S. K. Bhargav, B. Venkata Ramana, K. C. B. Rao and P. Siva Kumar, JNTUK, India; SAMEER, Chennai, India		
	P-72: Gigawatt Power Generation on KALI System using Relativistic Backward Wave Oscillator R. Chandra, V. Sharma, S. Singh, K. S. Senthil, S. Mitra, J. Mondal, A. Roy, A. S. Patel, R. Agarwal, A. Sharma, D. Biswas, R. Kumar, A. Vidyadhar, R. Singh & A. Shyam , BARC &IPR, Mumbai, India	P-01: Inverted L-Shaped Monopole MIMO Antenna for Multiband Applications S. Suganthi, P.T. Selvan and S.Raghavan KRCT, India; TRPEC, India; NIT Trichy, India		
			NIL	

	P-25: Resistive Sensor for High Power Microwave Pulse Measurement A. Allampalli and A. Bhattacharya, IIT KGP, Kharagpur, India	P-65: EMC Considerations in Designing Local oscillator for Wideband Receiver Systems V. Chattannavar and M. K. Arpana, BEL, Bengaluru, India	
12:20-12:50	PENARY TALK 3 Venue: Auditorium – I Topic: EMC Aspects in Product Design Life Cycle Speaker: Mr. Colin Brench, Amphenol TCS in Nashua, New Hampshire		
13:00-14:00	LUNCH BREAK		
14:00-15:20	Session 16: Design for EMI/EMC Venue: Auditorium – I Chairman: Mr. T Parthasarthy, Ex GD (EMOS), ADA, Bengaluru	Session 17: EMC Measurement and Testing Venue: Auditorium – II Chairman: Mr. Ganesh Kudava, TDK, USA	Venue: Auditorium – III
	P-64: Power Supply Systems in Defense: Practical Difficulties and Feasible Solutions P. Naik, A. Kumar and K. Rai, R&D Engrs, DRDO, Pune, India	P-43: Automation of Low frequency conducted susceptibility (CS01) test R.L. Mini, V.Raghavaiah, P.V.N. Murthy, R. Renuka, V.K.Hariharan and M.N. Rao, ISRO, Bengaluru, India	NIL
	P-34: Suppression of Simultaneous Switching Noise Using L-Bridge Electromagnetic Band Gap Structure K.Vishalatchiand N. Suresh Kuma; LMEC, VCET, India	P-27: A Study on the Evaluation of Harmonics and their effects during RS103 Radiated RF Immunity Testing on a C-band Transceiver W. B. Khan & L. Sudhakar, ASTRA Microwave, Hyderabad India	
	P-03: EMI/EMC Qualification of Differential Pressure Monitoring System for PFBR-A Case Study K. Praveen, A. D. Arun and M. Sivaramakrishna IGCEER, Kalpakam, India	P-28: Design & validation of Low-cost RF Current probe to estimate the Surface Transfer Impedance (STI) of Co-axial cable under test using LabVIEW software application P. R. Reddy, P. S. Kumar and K. C. B. Rao; JNTUK, SAMEER, India	
	P-77: EMC of Decoy Launch control panel for Mareech- A post design Case study K. R. Rao and M.Halder, RCI, Hyderabad, India	P-21: Changes in MIL-STD 461 Requirements & Tests and its applicability in present electromagnetic environment scenario C. Kumar, DEAL, Dehradun, India	
	P-48: Common Mode filter design for burst type pulses A. Kamath and B. Nayak, RBEL, Bengaluru, India	P-35: Accelerated Particle Swarm Optimization of Pattern Synthesis P. A. S.Dayal, K. Venkatesu, B. Anirudh and G.S.N. Raju GVPTC, India; AUCE, India; VIT, India	
15:30-16:30	Session 18: Panel Discussion Venue: Auditorium – I Topic: EMC Aspects in Product Design Life Cycle		
16:30-17:00	TEA BREAK		